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Date: April 1, 2005
To: Thao P. Le - 2818
Location: United States Patent and Trademark Office
Fax No.: (703) 872-9306
From: Cindy R. Simpson - 54,450
Subject: 10/716,956- Alexander B. Hoefler et al.

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MESSAGE:

Enclosed herewith, please find a FORMAL AMENDMENT for filing in the below-identified application.

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	x	1 page Facsimile Cover Sheet
2.	x	1 page Amendment

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PATENT AND TRADEMARK OFFICE:ON: 4-1-05
Date_____
SignaturePLEASE GIVE THESE PAPERS TO:EXAMINER: Thao P. Le
GROUP ART UNIT: 2818
SERIAL NO.: 10/716,956
FILED: NOVEMBER 19, 2003
INVENTOR: ALEXANDER B. HOEFLER ET AL.

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APPLICANT(S) Alexander B. Hoefler et al. GROUP ART UNIT: 2818
APPLN. NO.: 10/716,956 EXAMINER: Thao P. Le
FILED: November 19, 2003
TITLE: MULTI-BIT NON-VOLATILE INTEGRATED CIRCUIT MEMORY
AND METHOD THEREFOR

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AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated January 5, 2005, and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer.